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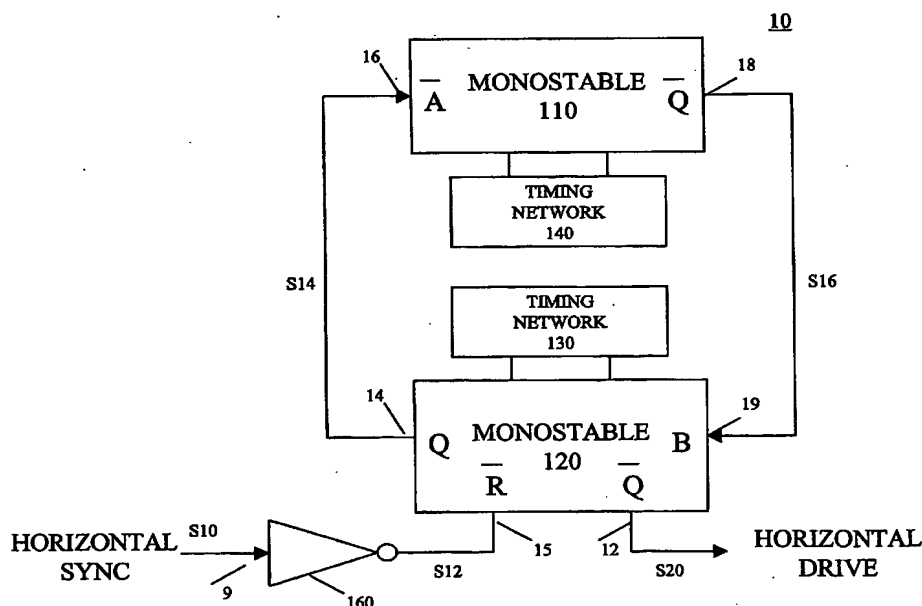
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(54) Title: **TIMING GENERATOR**



(57) Abstract: A timing generator and method of developing drive signals (S10) which, when input sync signals (S10) are present, delivers the leading and trailing edges of the input pulses (TS) directly to a drive output (TD) and which generates a free-running signal (S20) when input sync pulses are not present.